

Fig. 1

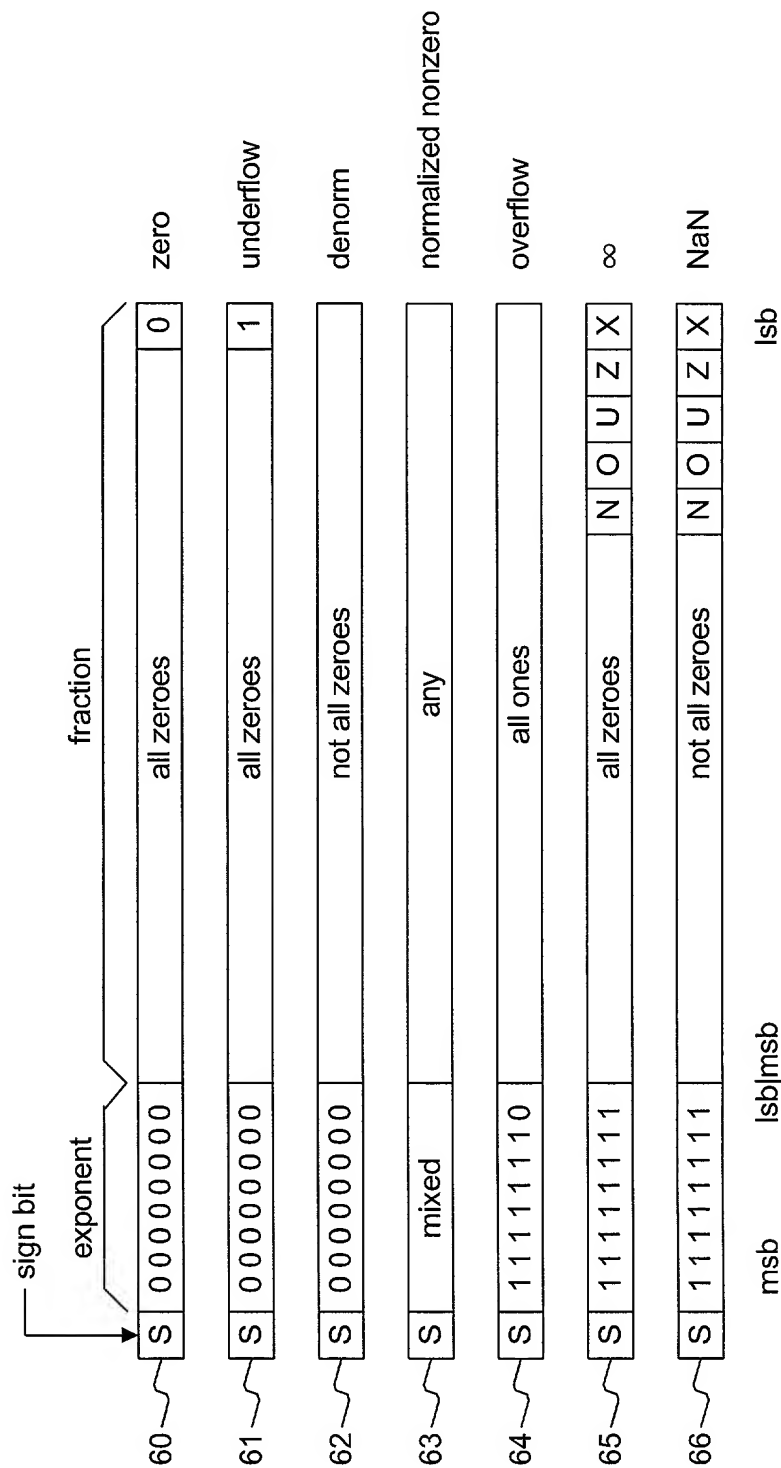


Fig. 2

+	$-\infty$	-OV	-Q	-UN	-0	0	+UN	+Q	+OV	$+\infty$	NaN
$-\infty$	(a)	(b)	(c)	(c)	(c)	(c)	(c)	(c)	(b)	(d)	(e)
-OV	(b)	-OV	-OV	-OV	-OV	-OV	(f)	(g)	(h)	(i)	(j)
-P	(c)	-OV	(k)	(l)	-P	-P	(m)	(n)	(o)	(p)	(q)
-UN	(c)	-OV	(l)	(r)	-UN	-UN	(s)	(l)	(t)	(p)	(u)
-0	(c)	-OV	-Q	-UN	-0	(v)	+UN	+Q	+OV	(p)	(q)
0	(c)	-OV	-Q	-UN	(v)	+0	+UN	+Q	+OV	(p)	(q)
+UN	(c)	(f)	(m)	(s)	+UN	+UN	(w)	(m)	+OV	(p)	(u)
+P	(c)	(g)	(n)	(l)	+P	+P	(m)	(x)	+OV	(p)	(q)
+OV	(b)	(h)	(o)	(t)	+OV	+OV	+OV	+OV	+OV	(i)	(j)
$+\infty$	(d)	(i)	(p)	(p)	(p)	(p)	(p)	(p)	(i)	(y)	(e)
NaN	(e)	(j)	(q)	(u)	(q)	(q)	(u)	(q)	(j)	(e)	(z)

Fig. 3

1-10----	[-NaN]	1-10----	[-NaN]	1--	*	0000	0	1	001	100000	11	00000	[-NaN	op1	f1 f2]
1-10----	[-NaN]	1-10----	[-NaN]	0--	*	0000	0	1	001	010000	11	00000	[-NaN	op2	f1 f2]
1-10----	[-NaN]	1-11----	[-Inf]	--	*	0000	0	1	001	100000	11	00000	[-NaN	op1	f1 f2]
1-10----	[-NaN]	1-1-1-	[-OV]	--	*	0000	0	1	001	100000	10	01001	[-NaN	op1	f1 ox]
1-10----	[-NaN]	1-1-1-1-	[-Q]	--	*	0000	0	1	001	100000	10	00000	[-NaN	op1	f1 ux]
1-10----	[-NaN]	11-1-1-	[-UN]	--	*	0000	0	1	001	100000	10	00101	[-NaN	op1	f1 f1]
1-10----	[-NaN]	11-1-1-	[-O]	--	*	0000	0	1	001	100000	10	00000	[-NaN	op1	f1 f1]
1-10----	[-NaN]	01-1-1-	[-+]	--	*	0000	0	1	001	100000	10	00101	[-NaN	op1	f1 f1]
1-10----	[-NaN]	01-1-1-	[-+UN]	--	*	0000	0	1	001	100000	10	00000	[-NaN	op1	f1 f1]
1-10----	[-NaN]	0-1-1-1-	[-+Q]	--	*	0000	0	1	001	100000	10	01001	[-NaN	op1	f1 ox]
1-10----	[-NaN]	0-1-1-1-	[-+OV]	--	*	0000	0	1	001	100000	10	01001	[-NaN	op1	f1 f2]
1-10----	[-NaN]	0-11----	[-+Inf]	--	*	0000	0	1	001	100000	11	00000	[-NaN	op1	f1 f2]
1-10----	[-NaN]	0-10----	[-+NaN]	1--	*	0000	0	0	001	100000	11	00000	[-NaN	op2	f1 f2]
1-10----	[-NaN]	0-10----	[-+NaN]	0--	*	0000	0	0	001	010000	11	00000	[-NaN	op2	f1 f2]
1-11----	[-Inf]	1-10----	[-NaN]	--	*	0000	0	1	001	010000	11	00000	[-NaN	op2	f1 f2]
1-11----	[-Inf]	1-11----	[-Inf]	--	*	0000	0	1	001	001000	11	00000	[-Inf	f1 f2]	
1-11----	[-Inf]	1-1-1-1-	[-OV]	--	*	0000	0	1	001	001000	10	01001	[-Inf	f1 ox]	
1-11----	[-Inf]	1-1-1-1-	[-Q]	--	*	0000	0	1	001	001000	10	00000	[-Inf	f1 f1]	
1-11----	[-Inf]	11-1-1-	[-UN]	--	*	0000	0	1	001	001000	10	00000	[-Inf	f1 f1]	
1-11----	[-Inf]	11-1-1-	[-O]	--	*	0000	0	1	001	001000	10	00000	[-Inf	f1 f1]	
1-11----	[-Inf]	01-1-1-	[-+]	--	*	0000	0	1	001	001000	10	00000	[-Inf	f1 f1]	
1-11----	[-Inf]	01-1-1-	[-+UN]	--	*	0000	0	1	001	001000	10	00000	[-Inf	f1 f1]	
1-11----	[-Inf]	0-1-1-1-	[-+Q]	--	*	0000	0	1	001	001000	10	01001	[-Inf	f1 ox]	
1-11----	[-Inf]	0-11----	[-+Inf]	0--	*	0000	0	0	001	000010	11	10000	[-Inf	f1 f2]	
1-11----	[-Inf]	0-11----	[-+Inf]	10-	*	0000	0	0	001	001000	11	00000	[-Inf	f1 f2]	
1-11----	[-Inf]	0-11----	[-+Inf]	--	*	0000	0	1	001	001000	11	00000	[-Inf	f1 f2]	
1-11----	[-Inf]	0-10----	[-+NaN]	--	*	0000	0	0	001	010000	11	00000	[-NaN	op2	f1 f2]
1-1-10----	[-OV]	1-1-10----	[-NaN]	--	*	0000	0	1	001	010000	01	01001	[-NaN	op2	f2 ox]
1-1-1-1-	[-OV]	1-11----	[-Inf]	--	*	0000	0	1	001	001000	01	01001	[-Inf	f2 ox]	
1-1-1-1-	[-OV]	1-1-1-1-	[-OV]	--	*	0000	0	1	010	000100	00	11111	[-OV]		
1-1-1-1-	[-OV]	1-1-1-1-	[-Q]	--	*	0000	0	1	010	000100	00	11111	[-OV]		
1-1-1-1-	[-OV]	11-1-1-	[-UN]	--	*	0000	0	1	010	000100	00	11111	[-OV]		
1-1-1-1-	[-OV]	11-1-1-	[-O]	--	*	0000	0	1	010	000100	00	11111	[-OV]		
1-1-1-1-	[-OV]	01-1-1-	[-+]	--	*	0000	0	1	010	000100	00	11111	[-OV]		
1-1-1-1-	[-OV]	01-1-1-	[-+UN]	0--	*	0000	0	1	010	000100	00	11111	[-OV]		
1-1-1-1-	[-OV]	01-1-1-	[-+UN]	10-	*	0111	1	0	000	000000	00	00000	[-HUGE+TINY]		
1-1-1-1-	[-OV]	01-1-1-	[-+UN]	11-	*	0000	0	1	010	000100	00	11111	[-OV]		
1-1-1-1-	[-OV]	0-1-1-1-	[-+Q]	--	*	0100	1	0	000	000000	00	00000	[-OV]		
1-1-1-1-	[-OV]	0-1-1-1-	[-+Q]	10-	*	0000	0	1	010	000100	00	11111	[-HUGE+Q]		
1-1-1-1-	[-OV]	0-1-1-1-	[-+OV]	11-	*	0000	0	1	010	000100	00	11111	[-OV]		
1-1-1-1-	[-OV]	0-1-1-1-	[-+OV]	10-	*	0000	0	0	001	000001	00	11001	[-HUGE+Q]		
1-1-1-1-	[-OV]	0-1-1-1-	[-+OV]	--	*	0000	0	1	010	000100	00	11111	[-OV]		
1-1-1-1-	[-OV]	0-11----	[-+Inf]	--	*	0000	0	0	001	000100	01	01001	[-HUGE+Q]		
1-1-1-1-	[-OV]	0-10----	[-+NaN]	--	*	0000	0	0	001	010000	01	01001	[-HUGE+Q]		
1-1-1-1-	[-OV]	0-10----	[-+NaN]	--	*	0000	0	0	001	010000	01	01001	[-HUGE+Q]		

Fig. 4A

11-----1	1-P	1-10----	[-NaN]	-	-	*	0000	0	1	001	010000	01	00000	[-NaN op2 f2]
11-----1	1-P	1-11----	[-Inf]	-	-	*	0000	0	1	001	001000	01	00000	[-Inf f2]
11-----1	1-P	1-1-----1	[-OV]	-	-	*	0000	0	1	010	000100	01	11111	[-OV]
11-----1	1-P	1-1-----1	[-Q]	-	-	0	0000	1	0	000	000000	00	00000	[IEEE 754 sum]
11-----1	1-P	1-1-----1	[-UN]	-	-	1	0000	0	1	010	000100	00	11111	[-OV]
11-----1	1-P	11-----1	[-UN]	-	0-	*	0000	1	0	000	000000	00	00000	[IEEE 754 sum]
11-----1	1-P	11-----1	[-UN]	-	10-	*	0001	1	0	000	000000	00	00000	[-P-0]
11-----1	1-P	11-----1	[-UN]	-	11-	*	0011	1	0	000	000000	00	00000	[-P-TINY]
11-----1	1-P	11-----1	[-0]	-	-	*	0000	1	0	000	000000	00	00000	[IEEE 754 sum]
11-----1	1-P	01-----1	[-UN]	-	-	*	0000	1	0	000	000000	00	00000	[IEEE 754 sum]
11-----1	1-P	01-----1	[-UN]	-	-	*	0000	1	0	000	000000	00	00000	[IEEE 754 sum]
11-----1	1-P	01-----1	[-UN]	-	0-	*	0011	1	0	000	000000	00	00000	[-P+TINY]
11-----1	1-P	01-----1	[-UN]	-	10-	*	0001	1	0	000	000000	00	00000	[-P+0]
11-----1	1-P	01-----1	[-UN]	-	11-	*	0000	1	0	000	000000	00	00000	[IEEE 754 sum]
11-----1	1-P	0-1-----1	[-UN]	-	-	*	0000	0	0	010	000100	00	11111	[-OV]
11-----1	1-P	0-1-----1	[-UN]	-	-	*	0000	0	0	010	000100	00	11111	[-OV]
11-----1	1-P	0-1-----1	[-UN]	-	10-	*	0000	0	0	010	000100	00	11111	[-P+HUGE]
11-----1	1-P	0-1-----1	[-UN]	-	11-	*	0001	1	0	000	000000	00	00000	[-Inf f2]
11-----1	1-P	0-11-----	[-Inf]	-	-	*	0000	0	0	001	001000	01	00000	[-NaN op2 f2]
11-----1	1-P	0-10-----	[-NaN]	-	-	*	0000	0	0	001	010000	01	00000	[-NaN op2 f2]
11-----1	1-P	1-10----	[-NaN]	-	-	*	0000	0	1	001	010000	01	00101	[-NaN op2 f2 ux]
11-----1	1-P	1-11----	[-Inf]	-	-	*	0000	0	1	001	001000	01	00000	[-Inf f2]
11-----1	1-P	1-1-----1	[-OV]	-	-	*	0000	0	1	010	000100	01	11111	[-OV]
11-----1	1-P	1-1-----1	[-Q]	-	0-	*	0000	1	0	000	000000	00	00000	[IEEE 754 sum]
11-----1	1-P	1-1-----1	[-UN]	-	10-	*	0100	1	0	000	000000	00	00000	[-0-Q]
11-----1	1-P	1-1-----1	[-UN]	-	11-	*	1100	1	0	000	000000	00	00000	[-TINY-Q]
11-----1	1-P	11-----1	[-UN]	-	0-	*	0000	0	1	100	001000	00	00001	[-UN]
11-----1	1-P	11-----1	[-UN]	-	10-	*	1111	1	0	000	000000	00	00000	[-2*TINY]
11-----1	1-P	11-----1	[-UN]	-	11-	*	0000	0	1	100	001000	00	00001	[-UN]
11-----1	1-P	11-----1	[-0]	-	-	*	0000	0	1	100	001000	00	00001	[-UN]
11-----1	1-P	01-----1	[-UN]	-	-	*	0000	0	1	100	001000	00	00001	[-UN]
11-----1	1-P	01-----1	[-UN]	-	0-	*	0000	0	0	100	001000	00	00001	[-UN]
11-----1	1-P	01-----1	[-UN]	-	10-	*	0000	0	0	100	001000	00	00001	[-UN]
11-----1	1-P	01-----1	[-UN]	-	11-	*	0000	0	1	100	001000	00	00001	[-UN]
11-----1	1-P	0-----1	[-UN]	-	0-	*	0000	1	0	000	000000	00	00000	[IEEE 754 sum]
11-----1	1-P	0-----1	[-UN]	-	10-	*	0100	1	0	000	000000	00	00000	[-0+Q]
11-----1	1-P	0-----1	[-UN]	-	11-	*	1100	1	0	000	000000	00	00000	[-TINY+Q]
11-----1	1-P	0-1-----1	[-UN]	-	0-	*	0000	0	0	010	000100	00	11111	[-OV]
11-----1	1-P	0-1-----1	[-UN]	-	10-	*	0000	0	0	010	000100	00	11111	[-OV]
11-----1	1-P	0-1-----1	[-UN]	-	11-	*	1101	1	0	000	000000	00	00000	[-TINY+HUGE]
11-----1	1-P	0-11-----	[-Inf]	-	-	*	0000	0	0	001	001000	01	00000	[-Inf f2]
11-----1	1-P	0-10-----	[-NaN]	-	-	*	0000	0	0	001	010000	01	00101	[-NaN op2 f2 ux]

Fig. 4B

11--1--	[-0]	1--10----	[-NaN]	-	-	*	0000	0	1	001	010000	01	00000	[-NaN op2 f2]
11--1--	[-0]	1--11----	[-Inf]	-	-	*	0000	0	1	001	001000	01	00000	[-Inf f2]
11--1--	[-0]	1--11----	[-OV]	-	-	*	0000	0	1	010	000100	01	11111	[-OV]
11--1--	[-0]	1--11----	[-Q]	-	-	*	0000	1	0	000	000000	00	00000	[IEEE 754 sum]
11--1--	[-0]	1--11----	[-UN]	-	-	*	0000	0	1	100	001000	00	00001	[-UN]
11--1--	[-0]	1--11----	[-0]	-	-	*	0000	0	1	100	001000	00	00000	[-0]
11--1--	[-0]	1--11----	[+0]	-	0-	*	0000	0	0	100	001000	00	00000	[+0]
11--1--	[-0]	1--11----	[+0]	-	10-	*	0000	0	0	100	001000	00	00000	[+0]
11--1--	[-0]	1--11----	[+UN]	-	11-	*	0000	0	1	100	001000	00	00000	[-0]
11--1--	[-0]	1--11----	[+Q]	-	-	*	0000	1	0	000	000000	00	00001	[+UN]
11--1--	[-0]	1--11----	[+OV]	-	-	*	0000	0	0	010	000100	00	11111	[+OV]
11--1--	[-0]	1--11----	[+Inf]	-	-	*	0000	0	0	001	001000	01	00000	[+Inf f2]
11--1--	[-0]	1--11----	[+NaN]	-	-	*	0000	0	0	001	010000	01	00000	[+NaN op2 f2]
01--1--	[+0]	1--10----	[-NaN]	-	-	*	0000	0	1	001	010000	01	00000	[-NaN op2 f2]
01--1--	[+0]	1--11----	[-Inf]	-	-	*	0000	0	1	001	001000	01	00000	[-Inf f2]
01--1--	[+0]	1--11----	[-OV]	-	-	*	0000	0	1	010	000100	01	11111	[-OV]
01--1--	[+0]	1--11----	[-Q]	-	-	*	0000	1	0	000	000000	00	00000	[IEEE 754 sum]
01--1--	[+0]	1--11----	[-UN]	-	-	*	0000	0	1	100	001000	00	00001	[-UN]
01--1--	[+0]	1--11----	[-0]	-	0-	*	0000	0	0	100	001000	00	00000	[+0]
01--1--	[+0]	1--11----	[+0]	-	10-	*	0000	0	0	100	001000	00	00000	[+0]
01--1--	[+0]	1--11----	[+UN]	-	11-	*	0000	0	1	100	001000	00	00000	[-0]
01--1--	[+0]	1--11----	[+Q]	-	-	*	0000	0	0	100	001000	00	00001	[+UN]
01--1--	[+0]	1--11----	[+OV]	-	-	*	0000	1	0	000	000000	00	00000	[IEEE 754 sum]
01--1--	[+0]	1--11----	[+Inf]	-	-	*	0000	0	0	010	000100	00	11111	[+OV]
01--1--	[+0]	1--11----	[+NaN]	-	-	*	0000	0	0	001	001000	01	00000	[+Inf f2]
01--1--	[+0]	1--10----	[-NaN]	-	-	*	0000	0	0	001	010000	01	00000	[+NaN op2 f2]

Fig. 4C

01----	1--	[+UN]	1-10----	[-NaN]	-	-	*	0000	0	1	001	010000	01	00101	[-NaN op2 f2 ux]
01----	1--	[+UN]	1-11----	[-Inf]	-	-	*	0000	0	1	001	001000	01	00000	[-Inf f2]
01----	1--	[+UN]	1-1--1-	[-OV]	-	0-	*	0000	0	1	010	000100	01	11111	[-OV]
01----	1--	[+UN]	1-1--1-	[-OV]	-	10-	*	1101	1	0	000	000000	00	00000	[+TINY-HUGE]
01----	1--	[+UN]	1-1--1-	[-OV]	-	11-	*	0000	0	1	010	000100	01	11111	[-OV]
01----	1--	[+UN]	1-1--1-	[-Q]	-	0-	*	0000	1	0	000	000000	00	00000	[IEEE 754 sum]
01----	1--	[+UN]	1-1--1-	[-Q]	-	10-	*	1100	1	0	000	000000	00	00000	[+TINY-Q]
01----	1--	[+UN]	1-1--1-	[-Q]	-	11-	*	0100	1	0	000	000000	00	00000	[+O-Q]
01----	1--	[+UN]	11--1-	[-UN]	-	0-	*	0000	0	0	100	001000	00	00001	[+UN]
01----	1--	[+UN]	11--1-	[-UN]	-	10-	*	0000	0	0	100	001000	00	00001	[+UN]
01----	1--	[+UN]	11--1-	[-UN]	-	11-	*	0000	0	1	100	001000	00	00001	[-UN]
01----	1--	[+UN]	11--1-	[-0]	-	-	*	0000	0	0	100	001000	00	00001	[+UN]
01----	1--	[+UN]	01--1-	[+0]	-	-	*	0000	0	0	100	001000	00	00001	[+UN]
01----	1--	[+UN]	01--1-	[+UN]	-	0-	*	0000	0	0	100	001000	00	00001	[+2*TINY]
01----	1--	[+UN]	01--1-	[+UN]	-	10-	*	1111	1	0	000	000000	00	00000	[+UN]
01----	1--	[+UN]	01--1-	[+UN]	-	11-	*	0000	0	0	100	001000	00	00001	[IEEE 754 sum]
01----	1--	[+UN]	0--1-	[+Q]	-	0-	*	0000	1	0	000	000000	00	00000	[+TINY+Q]
01----	1--	[+UN]	0--1-	[+Q]	-	10-	*	1100	1	0	000	000000	00	00000	[+O+Q]
01----	1--	[+UN]	0-1--1-	[+Q]	-	11-	*	0100	1	0	000	000000	00	00000	[+OV]
01----	1--	[+UN]	0-11--1-	[+OV]	-	-	*	0000	0	0	010	000100	00	11111	[+Inf f2]
01----	1--	[+UN]	0-11--1-	[+Inf]	-	-	*	0000	0	0	001	001000	01	00000	[+NaN op2 f2 ux]
01----	1--	[+UN]	0-10----	[+NaN]	-	-	*	0000	0	0	001	010000	01	00101	
0-10----	1	[+P]	1-10----	[-NaN]	-	-	*	0000	0	1	001	010000	01	00000	[-NaN op2 f2]
0-10----	1	[+P]	1-11----	[-Inf]	-	-	*	0000	0	1	001	001000	01	00000	[-Inf f2]
0-10----	1	[+P]	1-1--1-	[-OV]	-	0-	*	0000	0	1	010	000100	01	11111	[-OV]
0-10----	1	[+P]	1-1--1-	[-OV]	-	10-	*	0001	1	0	000	000000	00	00000	[+P-HUGE]
0-10----	1	[+P]	1-1--1-	[-Q]	-	11-	*	0000	0	1	010	000100	00	11111	[-OV]
0-10----	1	[+P]	11--1-	[-UN]	-	-	*	0000	1	0	000	000000	00	00000	[IEEE 754 sum]
0-10----	1	[+P]	11--1-	[-UN]	-	0-	*	0001	1	0	000	000000	00	00000	[IEEE 754 sum]
0-10----	1	[+P]	11--1-	[-UN]	-	10-	*	0011	1	0	000	000000	00	00000	[+P-O]
0-10----	1	[+P]	11--1-	[-0]	-	11-	*	0000	1	0	000	000000	00	00000	[+P-TINY]
0-10----	1	[+P]	11--1-	[+0]	-	-	*	0000	1	0	000	000000	00	00000	[IEEE 754 sum]
0-10----	1	[+P]	01--1-	[+UN]	-	-	*	0000	1	0	000	000000	00	00000	[IEEE 754 sum]
0-10----	1	[+P]	01--1-	[+UN]	-	0-	*	0011	1	0	000	000000	00	00000	[+P+TINY]
0-10----	1	[+P]	01--1-	[+UN]	-	10-	*	0000	1	0	000	000000	00	00000	[+P+O]
0-10----	1	[+P]	01--1-	[+Q]	-	11-	*	0001	1	0	000	000000	00	00000	[IEEE 754 sum]
0-10----	1	[+P]	0--1-	[+Q]	-	-	*	0000	0	0	010	000100	00	11111	[+OV]
0-10----	1	[+P]	0-11--1-	[+OV]	-	-	*	0000	0	0	001	001000	01	00000	[+Inf f2]
0-10----	1	[+P]	0-11--1-	[+Inf]	-	-	*	0000	0	0	001	001000	01	00000	[+NaN op2 f2]
0-10----	1	[+P]	0-10----	[+NaN]	-	-	*	0000	0	0	001	010000	01	00000	

Fig. 4D

[illegible]

Fig. 4E